UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,222,041 B2

APPLICATION NO.: 10/010547
DATED: May 22, 2007
INVENTOR(S): Cosmin Iorga

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, Line 19, Claim 5 currently reads as follows:

"A high-speed digital multiplexer according to clam 1" should read

-- A high-speed digital multiplexer according to claim 1 --

Column 5, Line 37, Claim 7 currently reads as follows:

"common voltage th branch connected to the common" should read

-- common voltage the branch connected to the common --

Column 5, Line 39, Claim 7 currently reads as follows:

"signal received by art enabled input pin" should read

-- signal received by an enabled input pin --

Column 5, Line 45, Claim 7 currently reads as follows:

"selected one of plurality of input pins based on the" should read

-- selected one of the plurality of input pins based on the --

Column 6, Line 6, Claim 12 currently reads as follows:

"A high-speed digital multiplexer according to claim 7" should read

-- A high-speed digital multiplexer according to claim 5 --

Column 6, Line 8, Claim 12 currently reads as follows:

"and at least one voltage of the non-selected ones of he" should read

-- and at least one voltage of the non-selected ones of the --

Column 6, Line 52, Claim 17 currently reads as follows:

"voltage then non-selected ones of the plurality of input pins." should read

-- voltage than non-selected ones of the plurality of input pins. --

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: May 22, 2007

hereby corrected as shown below:

DATED INVENTOR(S) : Cosmin Iorga

It is certified that error appears in the above-identified patent and that said Letters Patent is

Column 6, Line 54, Claim 18 currently reads as follows:

"wherein the common voltage is higher then the first voltage" should read

-- wherein the common voltage is higher than the first voltage --

Signed and Sealed this

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Thirty-first Day of July, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office